

TRANSMITTAL

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Title of Invention	METHOD OF MANUFACTURING GATE, THIN FILM TRANSISTOR AND PIXEL
Application Number :	
Date :	
First Named Applicant:	Mr. Ta-Jung Su
Confirmation Number:	
Attorney Docket Number:	13129-US-PA

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Submitted By:	Elec. Sign.	Sign. Capacity
Dr. Belinda Lee Registered Number: 46863	/Belinda Lee/	Agent

Documents being submitted:	Files
us-assignment	13129-usassn.xml us-assignment.xsl us-assignment.dtd 13129assignment01.tif 13129assignment02.tif
us-request	13129-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	13129-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
application-body	13129usf.xml application-body.dtd Image1.tif Image2.tif Image3.tif Image4.tif isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isogrk1.ent isogrk2.ent isogrk3.ent isogrk4.ent isolat1.ent isolat2.ent isomfrk.ent isomopf.ent isomscr.ent isonum.ent isopub.ent isotech.ent mathml2.dtd mathml2-qname-1.mod mmlalias.ent mmlextra.ent soextblk.dtd us-application-body.xsl wipo.ent
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